



VLSI CAD

Niranjan N.Manjunath Kotari Chiplunkar

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This well-organised book presents the basics of VLSI along with important algorithms used by CAD tool designers. It discusses general VLSI design styles, layout design rules, technology mapping in FPGAs and 3D-FPGAs. In addition, the text describes three important steps in high level synthesis of VLSI, namely, partitioning, scheduling, and data path allocation, besides logic synthesis which determines the gate level structure of circuits. Finally, the book gives a detailed account of physical synthesis, where steps such as floorplanning, placement, routing and compaction are explained with necessary algorithms.

This book is intended as a text for the undergraduate and postgraduate students of engineering—Electrical and Electronics Engineering/Electronics and Communication Engineering/Computer Science and Engineering, besides Instrumentation for their course on VLSI CAD. In addition, the book would also be extremely useful for professionals in this field.

KEY FEATURES :

- Presents a variety of chip design tools.
- Includes a fairly large number of algorithms.
- Discusses VHDL and graph theory essential for VLSI CAD tool design.
- Provides 100 questions selected from various university examination papers.

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